Form PTO-1449 (modified)
List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

ATTY. DKT. NO. 5500-66500

APPLICANT: McGrath, et al.

FILING DATE: April 2, 2001

SERIAL NO. 09/824,988

GROUP: 2185

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1	5,774,686	6/30/98	Hammond, et al.			
	A2	5,787,495	7/28/98	Henry, et al.			
	A3	5,784,638	7/21/98	Goetz, et al.			
	A4	4,620,274	10/28//86	Boothroyd, et al.			
	A5	5,826,074	10/20/98	Blomgren			
	A6	5,481,684	1/2/96	Richter, et al.			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	A7	Intel, "Intel Architecture Software Developer's Manual, Vol. 1: Basic Architecture," 1997, pages 3-1-3-15.
	A8	Intel, "Pentium Pro Family Developer's Manual, Vol. 3: Operating System Writer's Guide," 1996, pages 2-1-2-21, 3-1-3-33, and 4-1-4-29.
	A9	The Technology Behind Crusoe™ Processors, Low-Power x86-Compatible Processors Implemented with Code Morphing™ Software, Transmeta Corporation, January 2000, pages 1-18.
	A10	Alpha Learns to Do Windows; Digital's FX!32 is The Key To Running Win32 Software on Alpha/Windows NT., Selinda Chiquoine, BYTE, August 4, 1998, 4 pages.
	A11	Awards Stack Up for DIGITAL FX!32 Windows Compatibility Software for ALPHA, DIGITAL Press Releases, December 11,1 997, 7 pages.
	A12	DIGITAL FX!32; White Paper: How DIGITAL FX!32 Works, DIGITAL Semiconductor, January 26, 1998, 4 pages.
	A13	An Alpha in PC Clothing; Digital Equipment's New x86 Emulator Technology Makes An Alpha System a Fast x86 Clone, Tom Thompson, BYTE, August 4, 1998, 7 pages.
	A14	AMD 64-Bit Technology; The AMD x86 Architecture Programmers Overview, AMD, Publication #24108 Rev: A, August 2000, pages 1-106.
	A15	AMD 64-Bit Technology; The AMD x86-64 Architecture Programmers Overview, AMD, Pulication #24108 Rev: C, January 2001, pages 1-128.

EXAMINER: _____   DATE CONSIDERED: 6-26-04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
AL	B1	5,381,537	1/10/95	Baum, et al.			
AL	B2	5,481,684	1/2/96	Richter, et al.			
AL	B3	5,692,167	11/25/97	Grochowski, et al.			
AL	B4	4,701,946	10/20/87	Oliva, et al.			
AL	B5	5,617,554	4/1/97	Alpert, et al.			
AL	B6	5,758,116	5/26/98	Lee, et al.			
AL	B7	5,644,755	7/1/97	Wooten			
AL	B8	6,086,623	7/11/00	Broome, et al.			
	B9	5,517,651	5/14/96	Huck, et al.			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

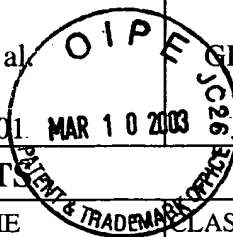|		
AL	B10	"Pentium Processor Family Developers Manual," Vol. 3, Architecture and Programming Manual, Intel, 1995, pp. 3-1 to 3-24, 10-1 to 10-13, 11-1 to 11-25.
AL	B11	"Advanced 80386 Programming Techniques," James L. Turley, Osborne McGraw-Hill, Berkeley, CA, 1988. pp. 45-84 and 283-315.

EXAMINER: _Aimee J. Li_   DATE CONSIDERED: _6/26/04_

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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
AL	C1	5,479,627	12/26/95	Khalidi, et al.			
						RECEIVED	
						MAR 1 2 2003	
						Technology Center 2100	

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EXAMINER: _Aimee A. Li_   DATE CONSIDERED: 6-26-04

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